

REMARKS/ARGUMENTS

Claims 1-13 are currently pending in the present patent application.

In a final Office Action mailed on May 3, 2006, the Examiner objected to the drawings as being informal and requested the submission of formal drawings. See Section 2 of the Office Action. Formal drawings accompany this amendment and are designated as replacement. These figures are believed to overcome the Examiner's objections to the drawings.

The Examiner also rejected claim 3, 4-6, and 8-13 under 35 U.S.C. § 112, first paragraph, as failing to comply with the enablement requirement. See Sections 4 and 5 of the Office Action. In Section 7, Claim 7 was rejected under 35 U.S.C. § 112, second paragraph, as being incomplete for omitting cooperative relationships among the elements.

Finally, in Sections 8 and 9 the Examiner rejected claims 1 and 2 under 35 U.S.C. § 102(b) as being anticipated by previously cited U.S. Patent No. 5,719,512 to Murayama ("Murayama").

With regard to claim 3, the recited functional blocks in Figure 2, for example, will be understood by those skilled in the art as to how these blocks function to perform the recited functions. One skilled in the art will understand how the claimed circuit generates the remove pulse signal from the other recited signals. The same is true with regard to elements recited in claims 4-6 and 8-13. A Rule 132 declaration may be submitted under MPEP 716.09 if the Examiner deems it necessary to overcome these rejections.

Claim 7 has been amended to expressly recite the interconnection between and among the recited elements. Any prior deficiencies under the second paragraph of Section 112 have been eliminated and this rejection should be withdrawn.

Turning now to the Murayama reference, the Murayama patent is directed to a circuit for extracting the color burst sinusoid, both frequency and phase, for an NTSC/PAL/SECAM composite video signal using a reference crystal, as previously described. Neither the burst signal applied on the input terminal 22 nor an output from a phase detector 21 generated in response to this burst signal and applied to an input terminal 14 is a "dithered" signal as recited in the claims. The term "dithered" is an understood term in the pertinent art and simply calling the burst signal applied on input terminal 22 or the output from the phase detector 21 on input terminal 14 in Murayama is

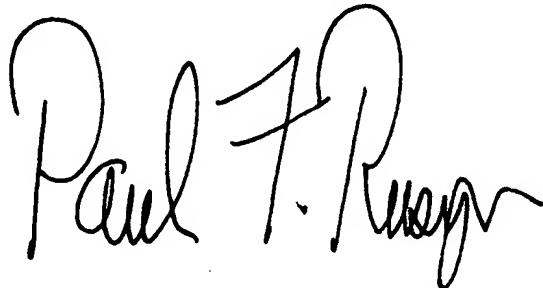
not giving the recited "dithered signal" its broadest reasonable interpretation. Dither is added to signals for a reason and a signal is either dithered or not. There simply is neither disclosure nor suggestion in Murayama that either of the signals on terminals 22 and 14 is a dithered signal.

For these reasons, Murayama neither discloses nor suggest the circuit recited in claim 1 and the combination of elements recited in claim 1 is accordingly allowable. Dependent claims 2-7 are allowable for at least the same reasons as claim 1 and due to the additional limitation added by these claims. The remaining claims, while not rejected in the Office Action as being anticipated by Murayama, are allowable for similar reasons.

The present patent application is in condition for allowance. Favorable consideration and a Notice of Allowance are respectfully requested. Should the Examiner consider any of the claims not in condition for allowance, Applicant respectfully requests the Examiner to contact the undersigned attorney at (425) 455-5575 to arrange for a telephone interview to resolve the outstanding issues.

Respectfully submitted,

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